

Amendments to the Specification:

Please replace the paragraph beginning at page 5, line 22 to line 26, with the following rewritten paragraph:

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--A hardware body generator also is coupled to the hardware information unit transfer controller. The hardware ~~header~~ body generator generates fields in a command information unit in response to signals from the hardware information unit transfer controller.--

Please replace the paragraph beginning at page 8, line 7 to line 21, with the following rewritten paragraph:

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--Packetized SCSI Protocol hardware packet engine 250 also eliminates the need for a processor or other on-chip hardware to move data from a sequencer control block (SCB), i.e., from a hardware I/O control block storage location, to a DMA channel prior to transmission to SCSI bus 270. Specifically, it is no longer necessary to move information from its storage location to another location for transmission to SCSI bus 270. The information is transmitted to SCSI bus 270 directly from its storage location. For example, information is transmitted directly from a stored hardware I/O control block. Therefore, packet engine 250 eliminates the prior art latency associated with ~~art~~ movement of information to enable transmission of the information to SCSI bus 270.--

Please replace the paragraph beginning at page 11, line 13 to line 29, with the following rewritten paragraph:

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--In this embodiment, each execution queue is a linked list of SCBs. Each target execution queue has a head pointer and a tail pointer. The head pointer points to the first SCB in the target execution queue, and the tail pointer points to

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the last SCB in the target execution queue. Each SCB contains a next SCB pointer that points to the next SCB in the target execution queue. The last SCB in the target execution queue has an invalid next SCB pointer, signifying that there are no more SCBs in that target execution queue. One embodiment of a SCB execution queue that can be used with the present invention is described in commonly assigned U.S. Patent Application Serial No. 09/587,538, entitled "Two-Dimensional Execution Queue for Host Adapters" of B. Arlen Young filed on June 1, 2000, now U.S. Patent No. 6,609,161 issued on August 19, 2003, which is incorporated herein by reference in its entirety.--

Please replace the paragraph beginning at page 12, line 21 to page 13, line 2, with the following rewritten paragraph:

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--If SCSI initiator 210 processes a single SCB format, the values in registers LUN offset 409, CDB length offset 411 and QNEXT pointer offset 413 are hardwired in packet engine 250. In another embodiment, offset registers 409, 411, and 413 are registers that are loaded by sequencer 220 in initialization operation 302. Note that initialization operation 302 is done only once and not for each SCB in the target execution queue. Each of these offsets is the distance from location of the SCB as specified by the value in the SCB pointer 254 to the start of the specified information. In this embodiment, the value in SCB pointer 254 is the number of the storage location in SCB array 280 and is the SCB identification number. LUN size scratch register 415 is also initialized. Typically, this is set to one byte. Finally, registers 407 and 408 are set to 00h.--